



PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

File the Application of

Attn: OIPE

Makoto KUDO

Application No.: 10/803,948

Docket No.: 118767

Filed: March 19, 2004

For: SEMICONDUCTOR DEVICE, SEMICONDUCTOR CIRCUIT, ELECTRONIC
EQUIPMENT, AND METHOD OF CONTROLLING CLOCK-SUPPLY

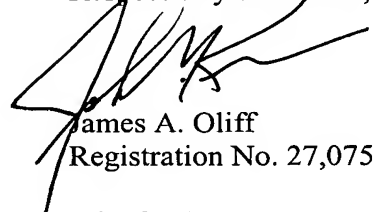
REQUEST FOR CORRECTION OF PALM RECORDS

Commissioner for Patents
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Sir:

Attached is a photocopy of the original filing receipt on which errors have been corrected in red. These errors are being brought to the attention of the Patent and Trademark Office so that it may correct its records. A Supplemental Application Data Sheet is attached.

Respectfully submitted,



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Date: July 29, 2004

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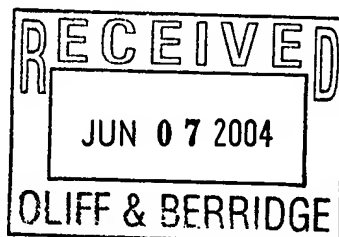


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APPL NO.	FILING OR 371 (c) DATE	ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO	DRAWINGS	TOT CLMS	IND CLMS
10/803,948	03/19/2004	2818	770	118767	8	8	3

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CONFIRMATION NO. 7180

FILING RECEIPT



OC000000012867065

Date Mailed: 06/04/2004

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Assignment For Published Patent Application
 SEIKO EPSON CORPORATION, Tokyo, JAPAN;

Domestic Priority data as claimed by applicant

Foreign Applications
 JAPAN 2003-078088 03/20/2003

If Required, Foreign Filing License Granted: 06/03/2004

Projected Publication Date: To Be Determined - pending completion of Missing Parts

Non-Publication Request: No

Early Publication Request: No

Title

Semiconductor device, semiconductor circuit, electronic equipment, and method of controlling clock-supply

Preliminary Class

365

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